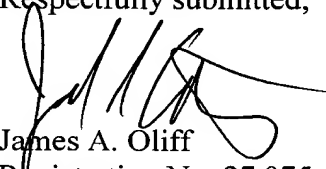


REMARKS

Claims 1-16 are pending. Claims 3, 5, and 10 are amended to eliminate multiple dependencies and claims 11-16 are amended to compensate for the subject matter deleted from claims 3, 5, and 10. The attached Appendix includes marked-up copies of each rewritten claim (37 C.F.R. 1.121(c)(1)(ii)). Prompt and favorable consideration on the merits is respectfully requested.

Respectfully submitted,

  
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JAO:JSA/kaf

Attachment  
Appendix

Date: May 30, 2001

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## APPENDIX

## Changes to Claims:

The following are marked-up versions of the amended claims:

3. (Amended) A memory device as claimed in claim 1 ~~or claim 2~~, wherein a common electrode is provided between two layers, an input electrode is provided on one of the layers and an output electrode is provided on the other of the layers, the input and output electrodes being disposed on opposite sides of their respective layers compared with the common electrode.

5. (Amended) A memory device comprising a plurality of devices as claimed in ~~any preceding claim~~ claim 1.

10. (Amended) A method as claimed in ~~any of claims 7 to 9~~ claim 7, wherein the step of detecting the said resultant voltage comprising the phase of the said resultant voltage with the phase of the said applied non-polarising voltage.

Claims 11-16 are added.

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